Title
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Permalink
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Journal
JOURNAL OF APPLIED PHYSICS, 110

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Publication Date
2011

DOI
10.1063/1.3651612

Supplemental Material
https://escholarship.org/uc/item/51j5b68c#supplemental

Peer reviewed
A heterojunction modulation-doped Mott transistor

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(Received 22 August 2011; accepted 12 September 2011; published online 18 October 2011)

A heterojunction Mott field effect transistor (FET) is proposed that consists of an epitaxial channel material that exhibits an electron correlation induced Mott metal-to-insulator transition. The Mott material is remotely (modulation) doped with a degenerately doped conventional band insulator. An applied voltage modulates the electron transfer from the doped band insulator to the Mott material and produces transistor action by inducing an insulator-to-metal transition. Materials parameters from rare-earth nickelates and SrTiO$_3$ are used to assess the potential of the “modulation-doped Mott FET” (ModMottFET or MMFET) as a next-generation switch. It is shown that the MMFET is characterized by unique “charge gain” characteristics as well as competitive transconductance, small signal gain, and current drive. © 2011 American Institute of Physics. [doi:10.1063/1.3651612]

I. INTRODUCTION

Electrostatic control of the competing electronic phases of transition metal oxides (“Mott materials”) that are close to a Mott metal-insulator transition may open an important arena for future electronics as well as provide an important tool for exploring the materials physics of highly correlated electron systems. There have been a number of proposals for field effect transistors (FETs) based on control of the Mott transition and some demonstrations of electrostatic gating of Mott materials. The potential advantages of a Mott FET have been pointed out—large on/off ratios and femtosecond switching speeds. An ideal Mott insulator is characterized by a half-filled $d$-band, corresponding to approximately one unpaired electron per unit cell, with the insulating ground state imposed by strong electron-electron repulsion. The insulating state is upset if extra electrons or holes are introduced, which move the system away from a precisely half-filled $d$-band. Typical estimates assume that complete suppression of the Mott insulating phase by electric-field-controlled electron accumulation or depletion requires very large charge carrier modulations, on the order of 0.1–0.3 carriers per unit cell. Electrolyte gating can introduce carrier densities of this order and has been used to substantially alter the electronic state of Mott materials like NdNiO$_3$ (Refs. 7 and 8) or to induce superconductivity in SrTiO$_3$. While electrolyte gating can induce a very large surface charge and is a powerful tool to explore the material physics of Mott material surfaces, it seems unlikely to be a viable approach for future electronics because the switching speed is limited by the low mobility of ions in the electrolytes.

In this letter, we explore the performance of a Mott heterojunction field effect transistor that features a Mott insulator/channel insulator interface and remote (modulation) doping of the Mott insulator. Modulation doping is widely used in high-mobility III-V semiconductor devices and has recently been applied to Mott insulators such as Sm$_2$CuO$_4$ and LaVO$_3$. Introduction of modest space charge through charge transfer across the heterojunction into the Mott insulator will drive the Mott channel into the conducting state. A voltage applied across the interface controls the electron transfer. We will refer to the proposed FET as a “modulation-doped Mott FET” or ModMottFET (MMFET). By judicious choice of operating temperature and, most importantly, the Mott material, the model suggests that only ~0.01–0.02 electrons per unit cell can release ~1 electron per unit cell as the device is turned on, an effective charge gain with implications for speed and transconductance. We will first discuss the concept of charge gain followed by a discussion of the MMFET performance using specific materials parameters.

II. MMFET DEVICE CHARACTERISTICS

Like other Mott FETs, but unlike any other 3-terminal FET, the MMFET is a unique “charge gain” device. Gating only 1%–10% of the channel electrons unleashes the entire conduction band of electrons in the Mott material. This is a completely new paradigm for device technology and in principle could lead to a pathway towards low power dissipation and ultra-fast devices as Si based transistors reach their limits. To illustrate the concept of charge gain, we compare the operation of the Mott FET with a traditional FET structure using a simple circuit model of a traditional FET structure and that of a MMFET (see supporting information). The voltage applied between the gate and source causes equal and opposite charges to be induced in the gate and the channel, with the channel charge participating in conduction. Transistor gain can be explained in the small signal approximation as follows: a small voltage $\Delta V$ causes a change in charge, $q\Delta n = C_g\Delta V$, which then changes the current. A current gain is achieved whenever the displacement current...
required to charge the gate-source capacitance \( (C_{gs}) \) is lower than the induced channel current.

In the case of a Mott FET, the electrostatic charge induced in the gate and channel is still equal (for charge neutrality). However, if a change of electron concentration leads to a Mott metal-insulator transition, then the electron density available for conduction may rise (or fall) by a few orders of magnitude. The total change in the \textit{conducting charge} can then be much greater than the charge imaged on the gate. While the magnitude depends on the details of the Mott transition induced in the material (see below), it can be up to 100 times greater than the original charge induced by the gate bias. Since the charge imaged at the gate is essentially “amplified” into a higher conducting carrier density, we term this phenomenon \textit{charge gain}. Assuming that the induced channel charge \( \Delta n \) is \( K \) times the charge imaged at the gate \( n_G \), the frequency for unity current gain (cutoff frequency \( f_c \)) of such a transistor is given by\(^{14} \)

\[
f_c = K \frac{v_{\text{eff}}}{2 \pi l},
\]

where \( v_{\text{eff}} \) is the effective velocity of electrons in the channel, and \( l \) is the gate length. In the Mott FET, the cutoff frequency is therefore \textit{enhanced} by a factor of \( K \) over the cutoff frequency of a purely capacitive FET.

For the purpose of discussing the performance of the MMFET, we use the known effect of \textit{chemical doping} on the phase transition and conductivity of a rare earth nickelate, NdNiO\(_3\). Rare earth nickelates (RNiO\(_3\), where \( R \) is a trivalent rare earth ion but not La) have been extensively studied in the bulk as they exhibit a sharp first order phase transition to an insulating state upon cooling.\(^{15} \) The transition temperature can be lowered by chemical doping.\(^{16,17} \) After accounting for lattice distortions that are also introduced by chemical substitution, the authors in Ref. 16 conclude that a 1\% change in electron or hole concentration suppresses the transition temperature by \( \sim 25 \) to \( 50 \) K for electrons or holes, respectively. Nevertheless, the pathways for the metal-to-insulator transition of the rare earth nickelates and for the origins of the doping-induced modulation of the transition temperature are still controversial. In particular, it is known that coupling to the lattice plays an important role in driving the transition in the rare earth nickelates.\(^{18-20} \) However, the concepts discussed here are general and apply to any Mott material exhibiting a metal-insulator transition controlled by band filling.

Figure 1 shows band diagrams of an epitaxial heterostructure that can be used to remotely dope a Mott material by charge transfer from a highly \( n \)-doped band insulator. For example, SrTiO\(_3\) can be degenerately \( n \)-doped by La or Nb to achieve carrier concentrations as high as \( 10^{21} \) cm\(^{-3}\).\(^{21,22} \) Before contact, as shown in Fig. 1(a), the Mott material (RNiO\(_3\)) is insulating as the operating temperature is \textit{just below} the metal-insulator transition temperature of the undoped material, \( T_{\text{MIT}} \) (200 K for NdNiO\(_3\)). The exact band offsets at RNiO\(_3\)/SrTiO\(_3\) interfaces are not well known and we assume \( \sim 1 \) eV.\(^{23} \) In contact, the Fermi levels align [Fig. 1(b)], and electrons transfer into the NdNiO\(_3\), depleting electrons in the SrTiO\(_3\). If sufficient electrons are transferred, the NdNiO\(_3\) near the interface will be metallic. The doping in the SrTiO\(_3\) is set back somewhat to mitigate leakage by tunneling when the junction is under bias. Figure 2 shows a schematic of a depletion mode MMFET.

The density of carriers transferred into the Mott material is greatest at the interface. The exponential decrease with distance from the interface can be described using Thomas-Fermi screening, which is characterized by a length, \( k_{TF}^{-1} \),

\[
k_{TF} = \sqrt{\frac{e (dn/d\mu)}{\varepsilon_{RNO}}},
\]

where \( e \) is the electron charge, \( \varepsilon_{RNO} \) is the dielectric permittivity of the nickelate (taken to be 30\( \varepsilon_0 \)), and \( dn/d\mu \) is the

![Figure 1](image-url)
density of states provided by the two hybridized $e_g$ orbitals per nickel (per unit cell). Assuming a band width for the oxygen-hybridized $e_g$ states of order of 6 eV, the density of states is estimated to be $5.2 \times 10^{27}$ m$^{-3}$ V$^{-1}$. In the metallic phase, the number of electrons transferred into the NdNiO$_3$ with a thickness $t$ is derived as follows:

$$n_{RNO} = \left(\frac{dn}{dt}\right) \int_0^t \phi_{RNO} \exp(-k_{TF}z)dz$$

$$= \sqrt{\left(\frac{dn}{dt}\right) \frac{e_{RNO}}{e} \phi_{RNO}[1 - \exp(-k_{TF}t)],}$$

where $\phi_{RNO}$ is the band bending in the RNiO$_3$. In the insulating phase, we assume that the density of states is much smaller than in the metallic phase but sufficient to locate the chemical potential near the middle of the Hubbard gap. Assuming no loss of charge to interface traps, the transferred electrons will equal in number those depleted in the La:SrTiO$_3$ as a function of position and applied voltage for a 1.5 nm thick RNiO$_3$ film that is interfaced with a SrTiO$_3$ film doped with $10^{21}$ cm$^{-3}$ of La. The carrier density of bulk RNiO$_3$ ($n_0$) is assumed to be 1 Ni $e_g$ electron per unit cell, or $\sim 1.8 \times 10^{22}$ cm$^{-3}$. The applied voltage is changed in increments on 0.1 V between 0 and 0.9 V. The dashed line indicates an excess carrier density of 1%. The inset shows of modulated carrier density at the interface as a function of La concentration in the SrTiO$_3$. (b) Current-voltage characteristics of the MMFET calculated from the parameters of chemically doped NdNiO$_3$, with the gate voltage as parameter. The labels indicate the gate voltage (in Volts), which is varied between 0 and 0.9 V. The transistor characteristics are terminated according to $V_{SD} + V_G < V_B$. Otherwise, the Schottky junction in the SrTiO$_3$ will be forward biased.

\[n_{RNO}(z,V) = n_{RNO}(0,V) \exp(-k_{TF}z)\]
The gate voltage dependent channel sheet conductivity can be calculated by using the known resistivity of NdNiO$_3$ as a function of chemical doping at 150 K (Ref. 16) (with other RNiO$_3$’s, $T_{MIT}$ can be shifted to near room temperature or above$^{15}$). The total sheet channel conductivity, $g_c$, was obtained by summing the two-dimensional conductivity, $\sigma_{2D}$, of layers of thickness $t_n$, using the transferred electron density as a function of position.$^{14}$

$$g_c = \sum \sigma_{2D}.$$  \(7\)

From the sheet conductivity, the transistor drain current vs. drain voltage ($I_{DS}$ vs. $V_{SD}$) characteristics can be calculated for $V_{DS} < V_b - V_G$ by the following equation:

$$I_{DS} = \frac{w}{L} \int_0^{V_{DS}} dV \, g_c(V + V_G),$$  \(8\)

where $L$ is the channel length (taken to be 30 nm in the calculation) and $w$ is the width. Figure 3(b) shows the device characteristics as a function of $V_{DS}$. The device is essentially a depletion mode device. An applied gate voltage removes the excess electrons, returning the Mott material to the insulating (“off”) state. Although the device, as configured, is limited for positive source-drain characteristics, the transconductance for both positive and negative source drain bias is large, of the order of $g_m = (\partial I_{DS}/\partial V_G)_{V_{SD}=const} \approx 3000$ mS/mm at a source-drain voltage of $V_G$ of $+/-0.45$ V. This is substantially higher than $g_m$ values of state-of-the-art Si FETs. We also note that the device will also support large current drive, several amperes/mm.

### III. PRACTICAL CONSIDERATIONS AND CONCLUSIONS

A depletion mode device is perhaps the simplest manifestation of a MMFET. Using a wider band gap insulator in place of the set back layer would open the possibility of enhancement mode operation. A p-channel device would follow if the interfacial region of the MMFET were chemically doped with divalent cations.$^{16}$ Finally, we note that the MMFET is intrinsically an ambipolar device, which may encourage electronic applications beyond switches.$^{24}$

Towards practical realization of the MMFET, critical materials issues must be addressed. Foremost, despite extensive work on chemical doping, which purports to separate the effect of band filling from lattice distortions on the metal insulator transition, experimental demonstration of pure band filling control is limited. The MIT of the rare earth nickelates in particular may be more sensitive to lattice distortions than to a change in band filling. Thus, the most important goal towards the realization of any Mott FET should be research that is directed to identify suitable Mott materials that respond to a change in band filling. There are many possible candidate materials in addition to the rare earth nickelates. For example, the rare earth titanates are well known to show metal-insulator transitions that are associated with orders of magnitude change in conductivity upon hole doping.$^{25}$ Further, there is little documentation of band offsets in relevant oxide heterostructures. Disorder induced scattering and disorder broadening of the phase transition will compromise any phase change Mott FET. The effects of quantum confinement and surface scattering are largely unexplored. It should be noted, however, that for the MMFET, only the interfacial layer plays a role; thicker epilayers would mitigate at least the effects of surface roughness.

### ACKNOWLEDGMENTS

The authors gratefully acknowledge support by a MURI program of the Army Research Office (Grant # W911-NF-09-1-0398) as well as useful discussions with Mark Rodwell.

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14. See supplementary material at http://dx.doi.org/10.1063/1.3651612 for a derivation of the cut-off frequency and an estimate of the channel conductivity.